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**SUPPLEMENTAL INFORMATION
DISCLOSURE STATEMENT BY
APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known

Application Number	10/590,876
Filing Date	July 10, 2007
First Named Inventor	Hartmut SCHUMACHER
Art Unit	2836
Examiner Name	Michael Rutland Wallis
Attorney Docket Number	10191/4350

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number Number - Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁰
/M.R.W./		DE 35 06 487	September 4, 1986			*
/M.R.W./		WO 03/059681	July 24, 2003			*

* English language abstract provided.

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/M.R.W./		Tietze: Semiconductor Switching Technology, 12 th Edition, Springer-Publishing 2002, ISBN 3-540-42849-6, pages 947, 957-960, particularly 959**	

** English language translation of page 959 provided herewith.

Examiner
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/Michael Rutland Wallis/

Date
Considered

08/05/2011

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